WHAT IS CLAIMED IS:

1. A multi-display projector comprising:

an input pattern memory for storing input format parameters which specify the number of active pixels, the number of active lines, the initial active pixel, and the active initial line of input image signals having arbitrary formats, said format parameters being stored for each of said formats;

a frame memory for storing active image signals extracted from said input image signals based on said input format parameters;

a display pattern memory for storing display parameters which designate a region of an image to be displayed; and

a displaying means which displays said region of the image by processing said active image signals stored in said frame memory based on said display parameters.

2. A multi-display projector claimed in claim 1, wherein:

said display parameter further including the horizontal offset and the vertical offset which designate the amounts of displacement of the display position when the display position of an image is displaced horizontally and vertically;

and the position of a display image in the displaying means

is adjusted by changing the values of said horizontal offset
and said vertical offset.

3. A multi-display projector claimed in claim 1,
further comprising:
an A/D converter for converting analog image signals to
digital image signals, wherein the input pattern memory
stores parameters of said A/D converter based on which said
analog image signals are converted to said digital image
signals.

4. A multi-display system comprising:
 a plurality of multi-display projectors as claimed in claim
1, said multi-display projectors being arranged in both
horizontal and vertical direction;
 and controlling means for controlling the operation of each
of said multi-display projectors.